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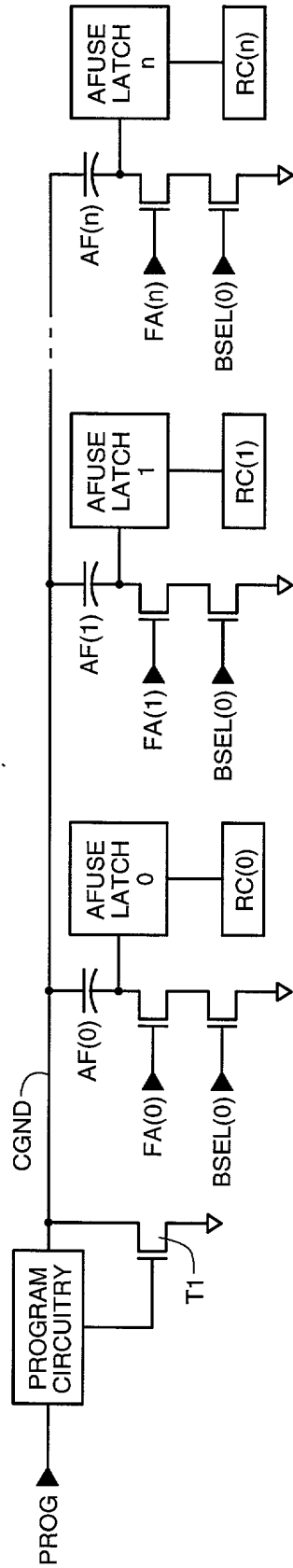


FIG. 1
(RELATED ART)

FIG. 1 (RELATED ART)

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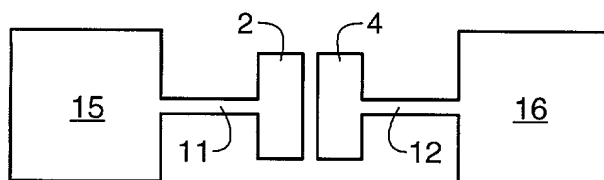


FIG. 2A

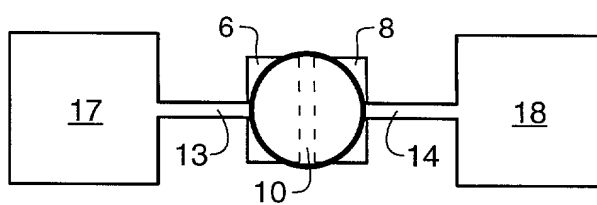


FIG. 2B

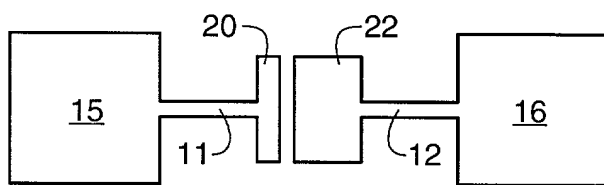


FIG. 3A

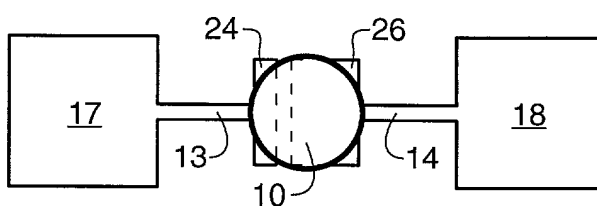


FIG. 3B

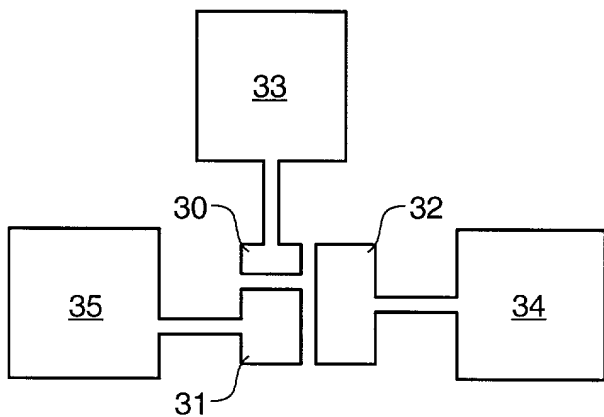


FIG. 4A

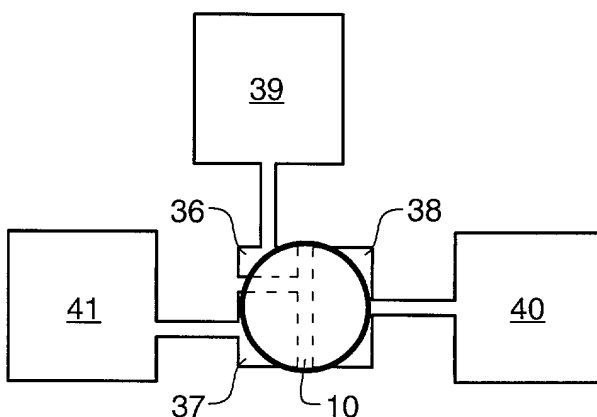


FIG. 4B

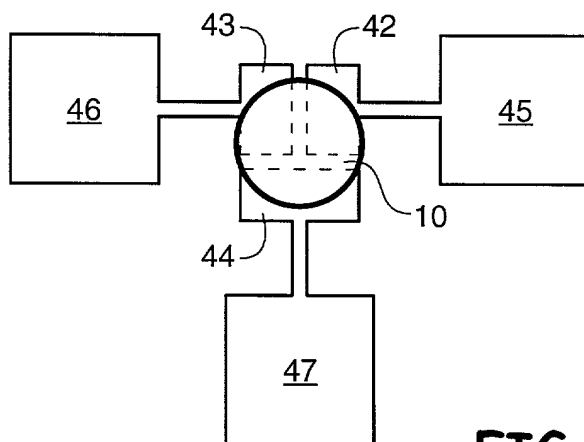


FIG. 5

FIG. 2A

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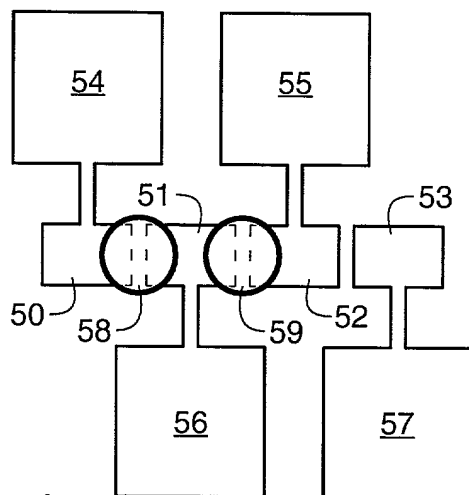


FIG. 6

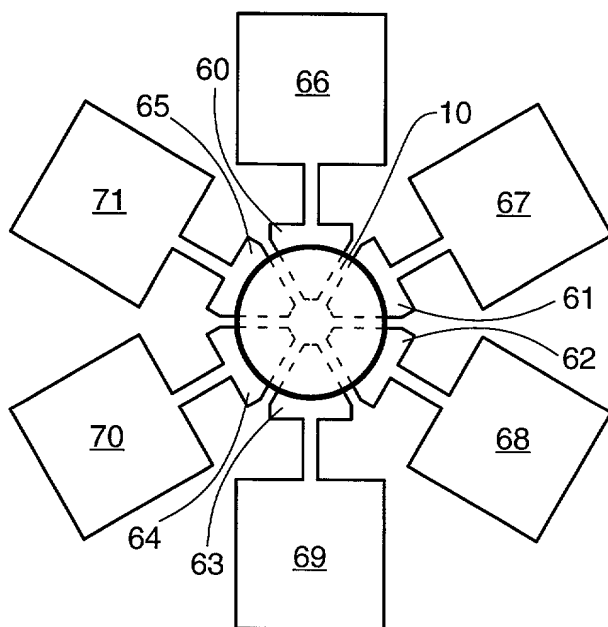


FIG. 7

FIG. 6

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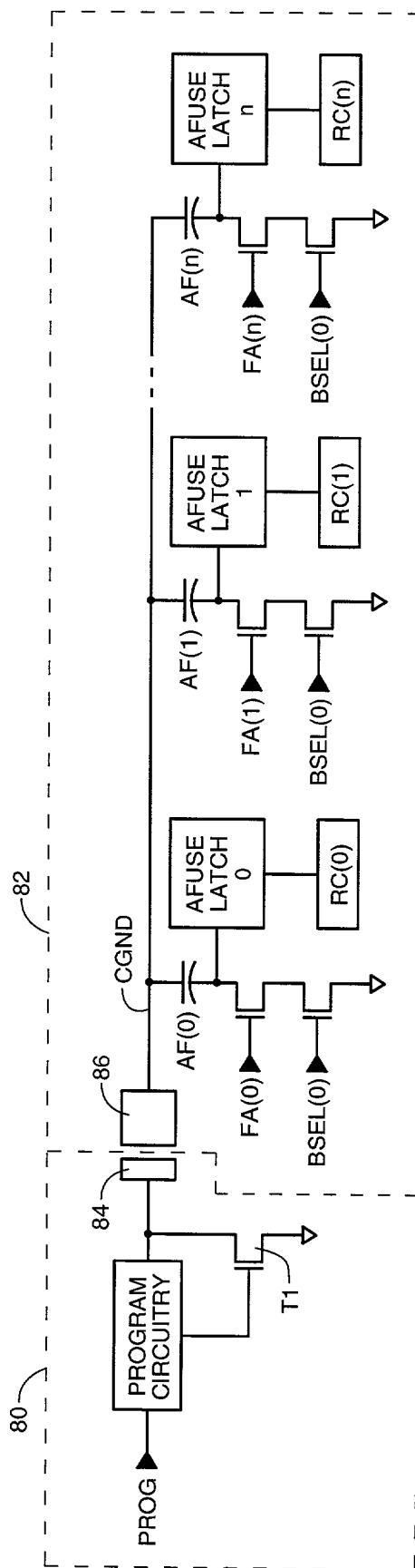
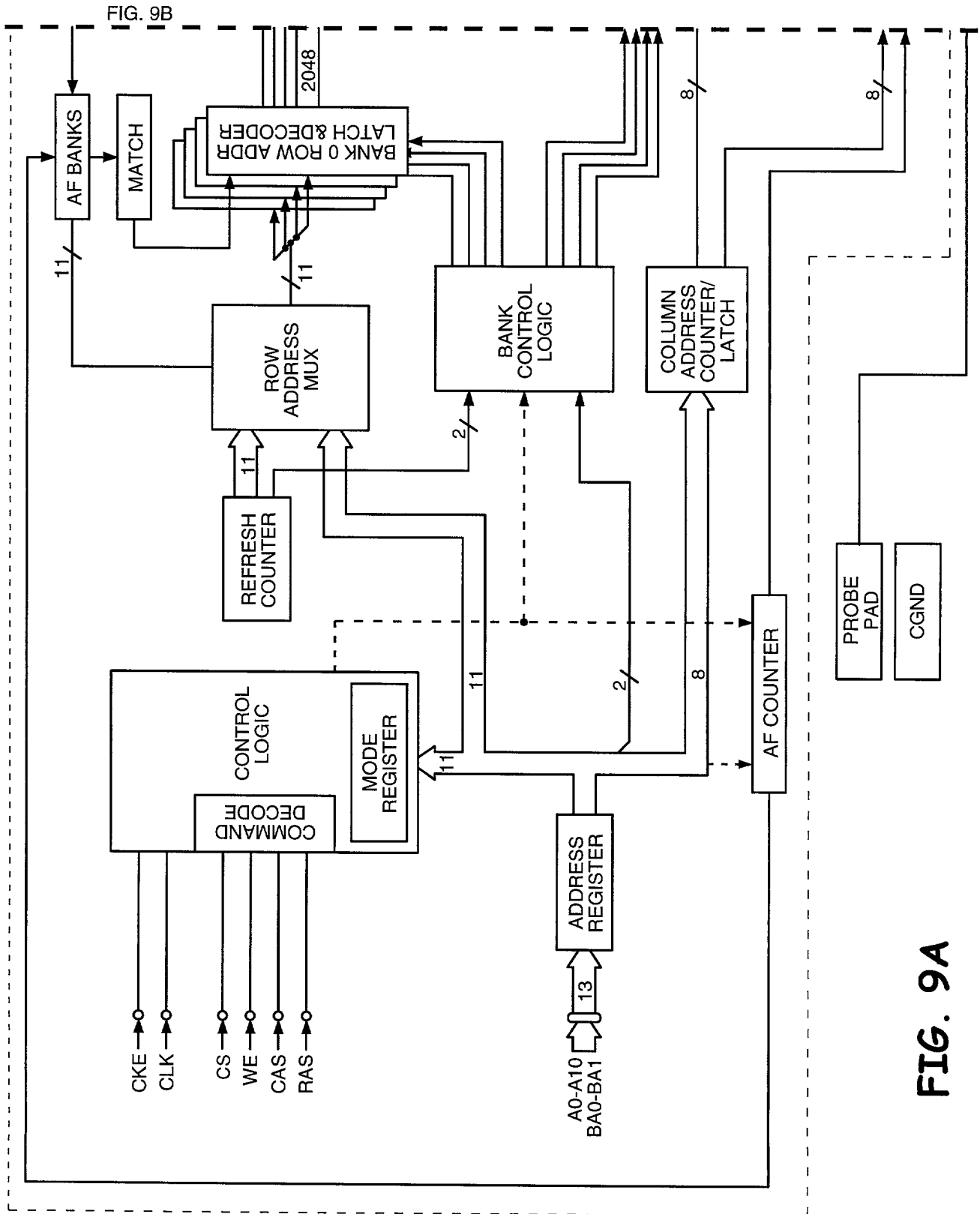


FIG. 8

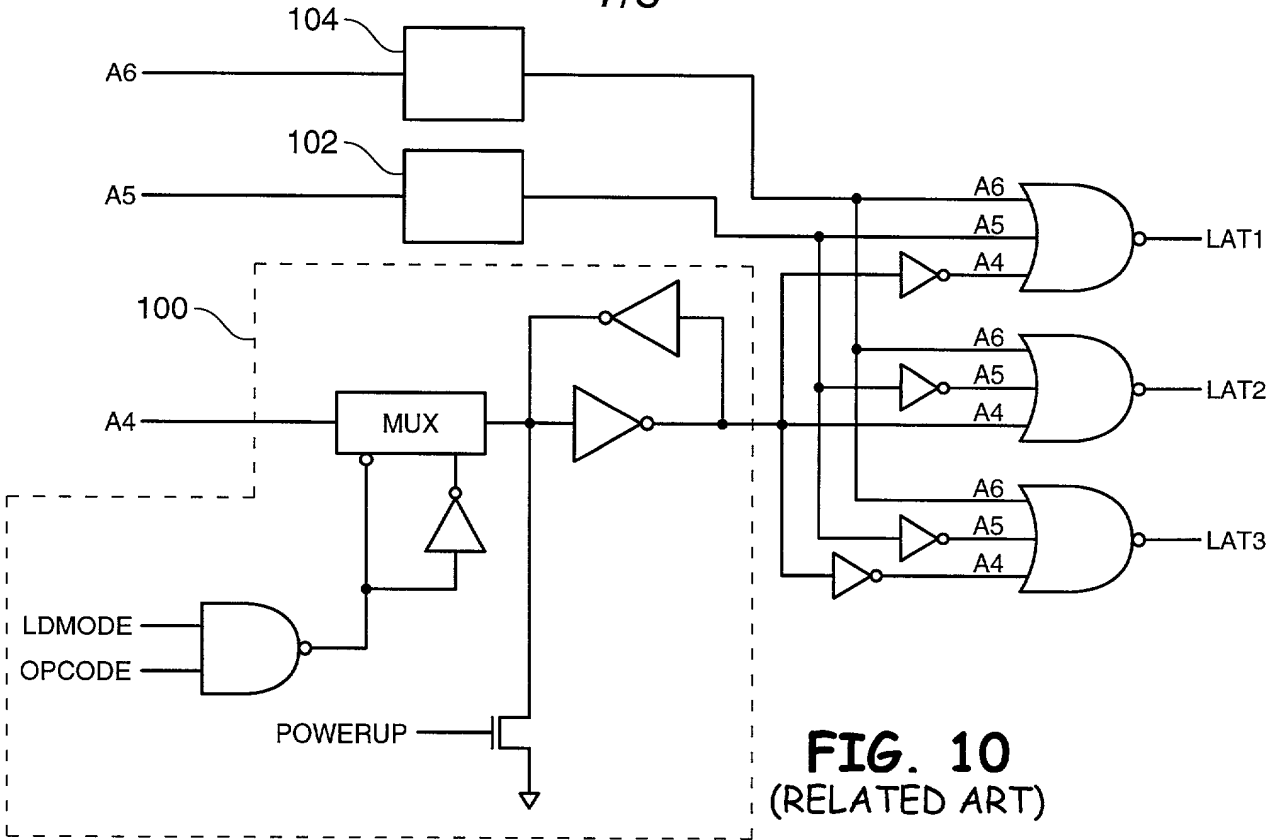
FIG. 8

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A6	A5	A4	LATENCY
0	0	1	1
0	1	0	2
0	1	1	3

FIG. 11

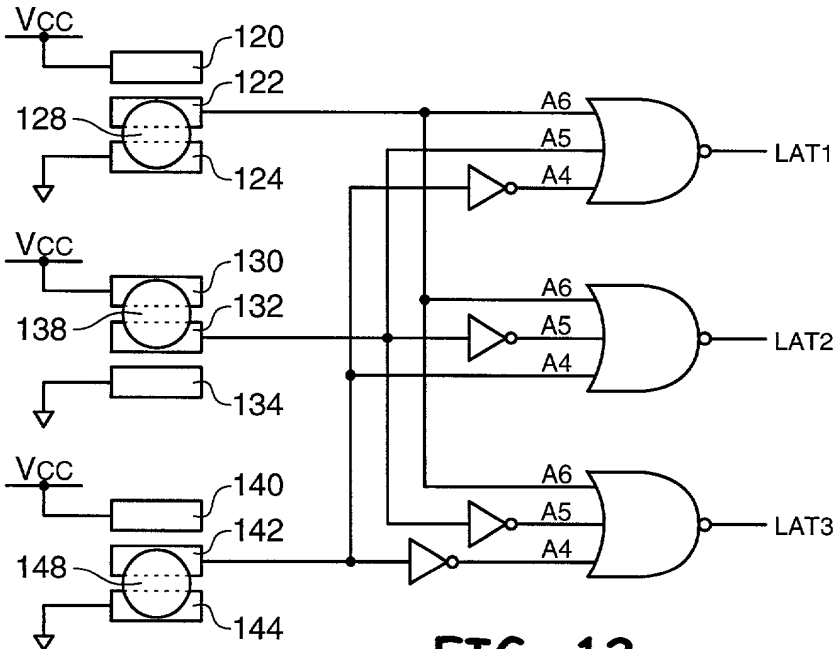


FIG. 12

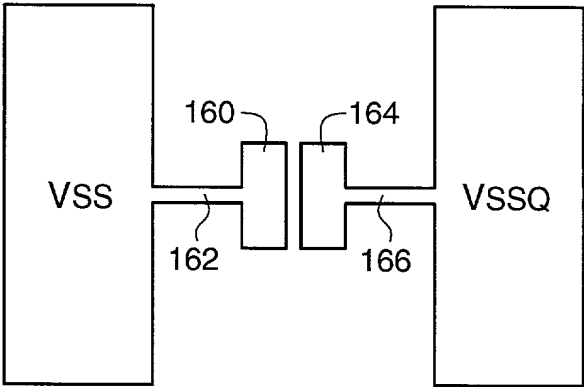


FIG. 13A

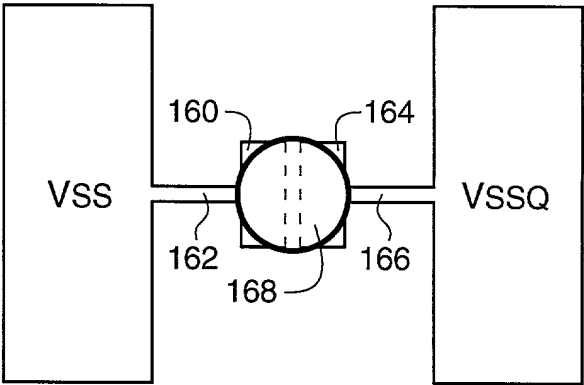


FIG. 13B

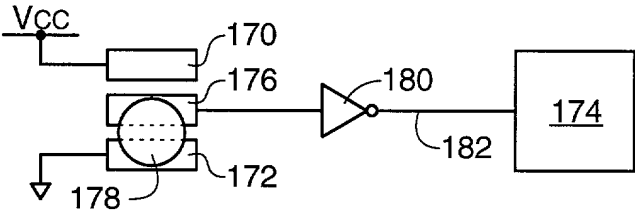


FIG. 14

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